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Joseph F. Hetz - Reg. No. 41,070

Name of Applicant, Assignee or
Registered Representative


Signature

Patent
Our Case No. 10519-110

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
Moore et al.)	
)	Group Art Unit: 2177
Serial No.: 10/674,289)	
)	
Filed: September 29, 2003)	
)	
For: Method for Deleting Stored)	
Digital Data from Write-Once)	
Memory Device)	

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to the obligation under 37 C.F.R. § 1.56 and in conformance with 37 C.F.R. §§
1.97-1.99, Applicants hereby submit documents A1-A37 listed on the attached form PTO-1449
for consideration by the Examiner. Applicants request that the Examiner review the entire
disclosure of these documents and make them of record. In accordance with 37 C.F.R. § 1.98(d),
copies of documents A2-A37 are not enclosed since they were previously cited by or submitted

to the Office in prior application serial number 09/638,439, which is being relied upon for an earlier filing date under 35 U.S.C. § 120. A copy of the parent patent is enclosed as document A1.

Applicants also wish to note that pending Claims 1 and 2 correspond to Claims 1 and 3 in parent application serial number 09/638,439. Those claims were rejected in the October 3, 2002 Office Action. Applicants request that the Examiner review those rejections when examining the present application.

The filing of this Information Disclosure Statement does not constitute an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, Applicants reserve the right to contest that any of the information submitted herewith is prior art against the present application.

Dated: January 7, 2004

Respectfully submitted,



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FORM PTO-1449	SERIAL NO. 10/674,289	CASE NO. 10519-110
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	FILING DATE September 29, 2003	GROUP ART UNIT 2177
(use several sheets if necessary)		APPLICANT(S): Moore et al.

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER <small>Number-Kind Code (if known)</small>	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
	A1	6,658,438 B1	12/02/03	Moore et al.		
	A2	4,272,880	06/16/81	Pashley		
	A3	4,442,507	04/10/84	Roesner		
	A4	4,489,478	12/25/84	Sakurai		
	A5	4,498,226	02/12/85	Inoue et al.		
	A6	4,499,557	02/12/85	Holmberg et al.		
	A7	4,646,266	02/24/87	Ovshinsky et al.		
	A8	4,677,742	07/07/87	Johnson		
	A9	5,070,384	12/03/91	McCollum et al.		
	A10	5,306,935	04/26/94	Esquivel et al.		
	A11	5,427,979	06/27/95	Chang		
	A12	5,441,907	08/15/95	Sung et al.		
	A13	5,535,156	07/09/96	Levy et al.		
	A14	5,602,987	02/11/97	Harari et al.		
	A15	5,640,343	06/17/97	Gallagher et al.		
	A16	5,745,407	04/28/98	Levy et al.		
	A17	5,835,396	11/10/98	Zhang		
	A18	6,034,882	03/07/00	Johnson et al.		
	A19	5,563,947	10/08/96	Kikinis		
	A20	5,596,639	01/21/97	Kikinis		
	A21	5,065,389	11/12/91	Roth		
	A22	6,321,360 B1	11/20/01	Takeuchi et al.		
	A23	6,377,526 B1	04/23/02	Vining et al.		
	A24	5,448,372	09/95	Axman et al.		
	A25	5,241,659	08/93	Parulski et al.		
	A26	5,315,178	05/94	Snider, Gregory S.		
	A27	4,691,299	09/87	Rivest et al.		
	A28	4,868,804	09/89	Le Carvennec et al.		
	A29	6,104,638	08/00	Larner et al.		
	A30	5,029,125	07/91	Sciupac, Luis H.		

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER <small>Number-Kind Code (if known)</small>	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES OR NO
	A31	WO 99/14763	03/25/99	PCT		Yes

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1449	SERIAL NO. 10/674,289	CASE NO. 10519-110
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	FILING DATE September 29, 2003	GROUP ART UNIT 2177
(use several sheets if necessary)		APPLICANT(S): Moore et al.

EXAMINER INITIAL	OTHER ART – NON PATENT LITERATURE DOCUMENTS (Include name of author, title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date page(s), volume-issue number(s), publisher, city and/or country where published.	
	A32	Douglas, John H., "The Route To 3-D Chips", High Technology, Sept. 1983, Vol. 3, No. 9, pp. 55-59
	A33	Edited by Cappelletti, Paulo et al., "Flash Memories", Kluwer Academic Publishers, 1999, p. 47, p. 308
	A34	Kawashima, Shoichiro et al., "A Charge-Transfer Amplifier and an Encoded-Bus Architecture for Low-Power SRAM's", IEEE Journal of Solid-State Circuits, Vol. 33, No. 5, May 1998, pp. 793-799
	A35	Zhang, Ph.D., Guobiao, "Three-Dimensional Read-Only Memory (3D-ROM)", presentation from website zhangpatents, pp. 1-29
	A36	Zhang, Ph.D., Guobiao, "3D-ROM – A First Practical Step Towards 3D-IC" Semiconductor International, July 2000, from website zhangpatents, pp. 1-7
	A37	The Norton Desktop (Version 3 for Windows) User's Guide, pp. 16-1 – 16-6 (1993)

EXAMINER	DATE CONSIDERED
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